

1/9

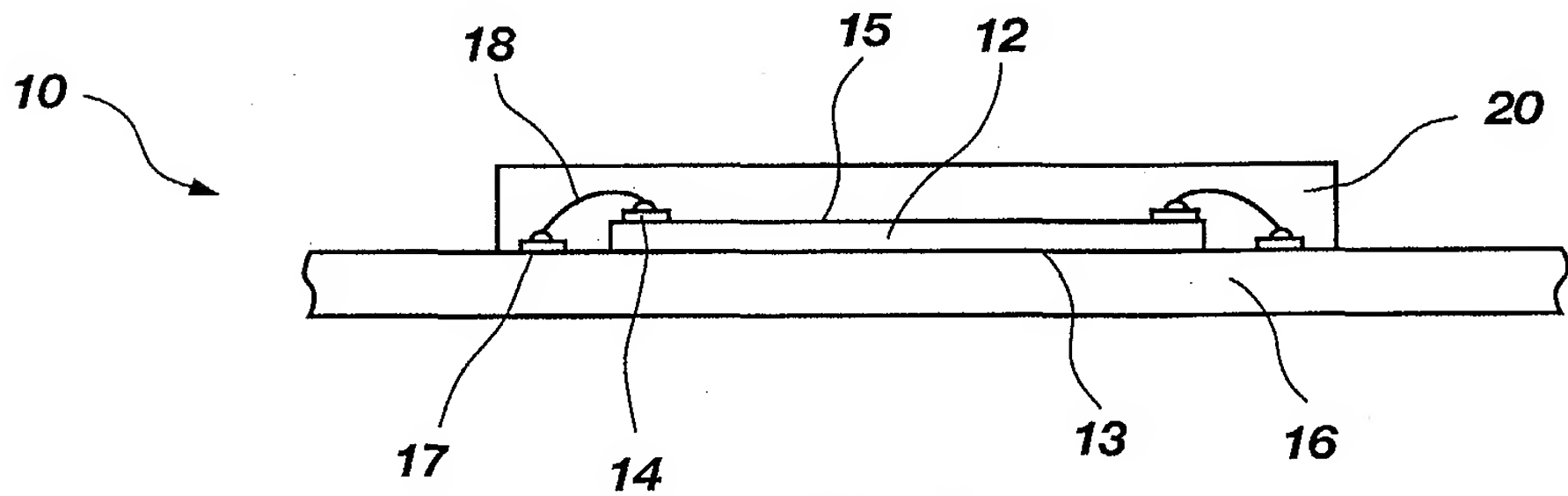


Fig. 1

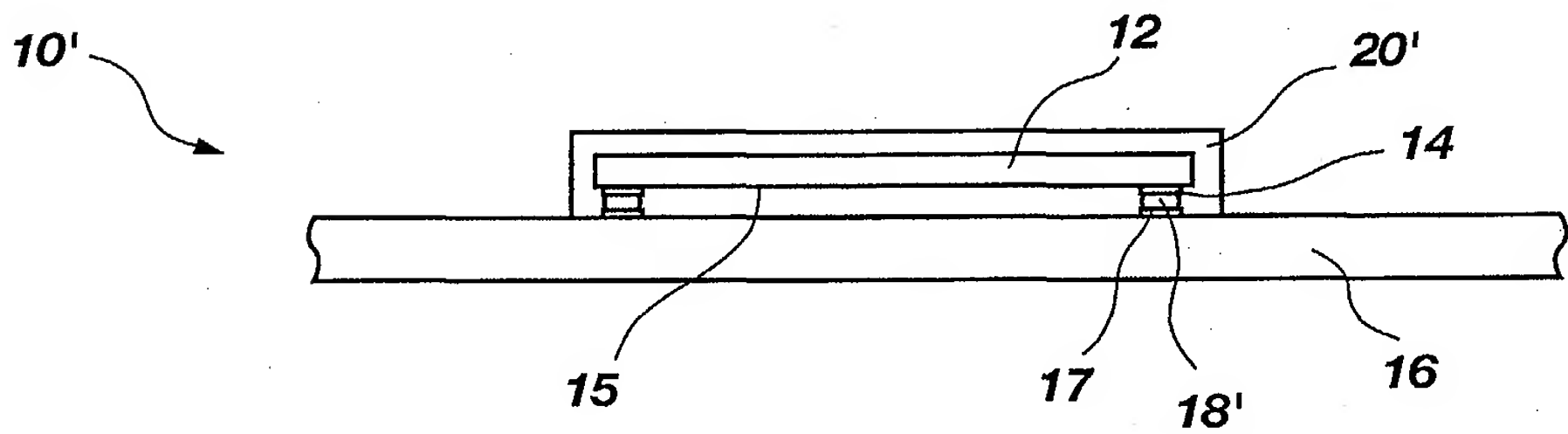


Fig. 2

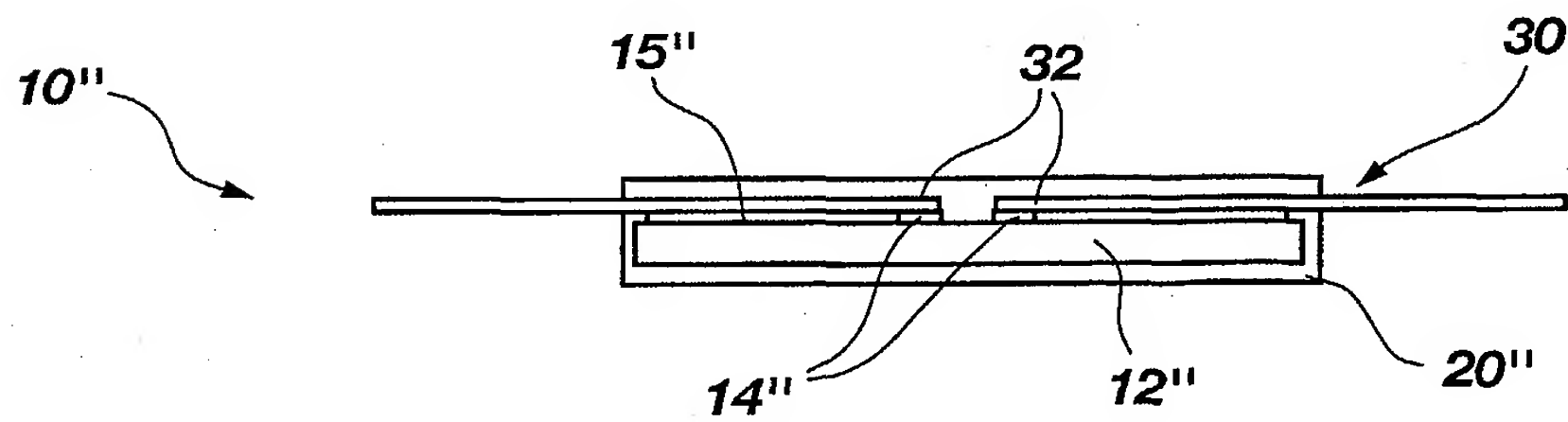


Fig. 3

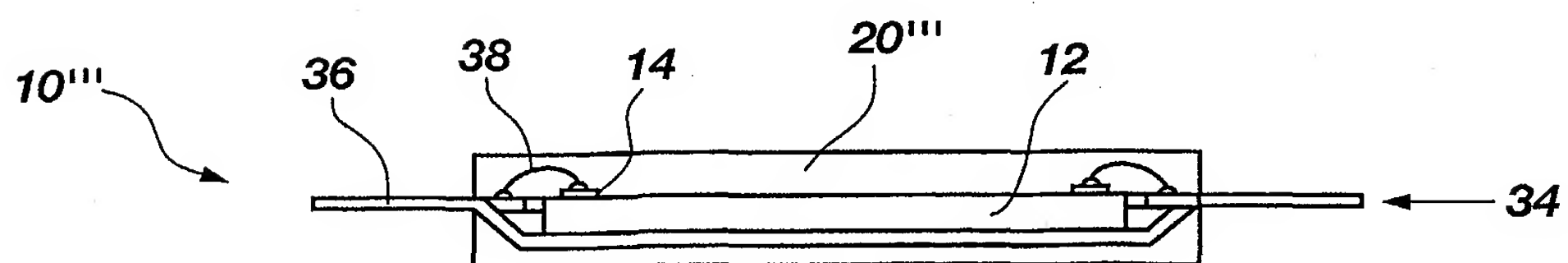


Fig. 4

TITLE: STEREOLITHOGRAPHIC METHODS FOR FABRICATING
HERMETIC SEMICONDUCTOR DEVICE PACKAGES AND
SEMICONDUCTOR DEVICES INCLUDING
STEREOLITHOGRAPHICALLY FABRICATED HERMETIC PACKAGES

Inventor: Warren M. Farnworth

Serial No.: Not Yet Assigned

Docket No.: 4212.1US

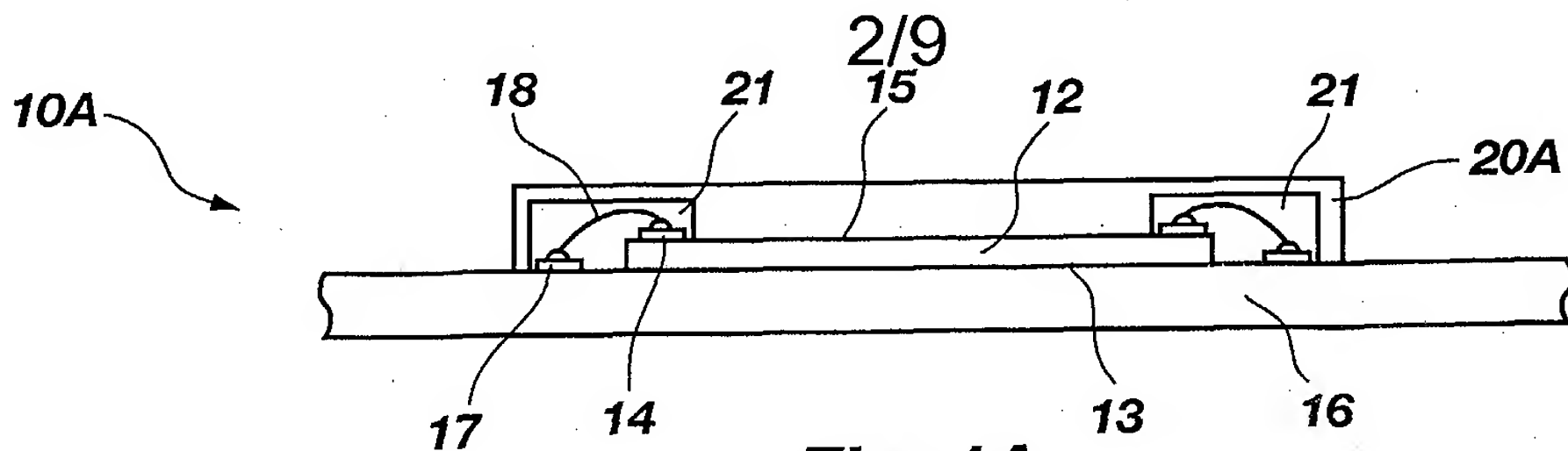


Fig. 1A

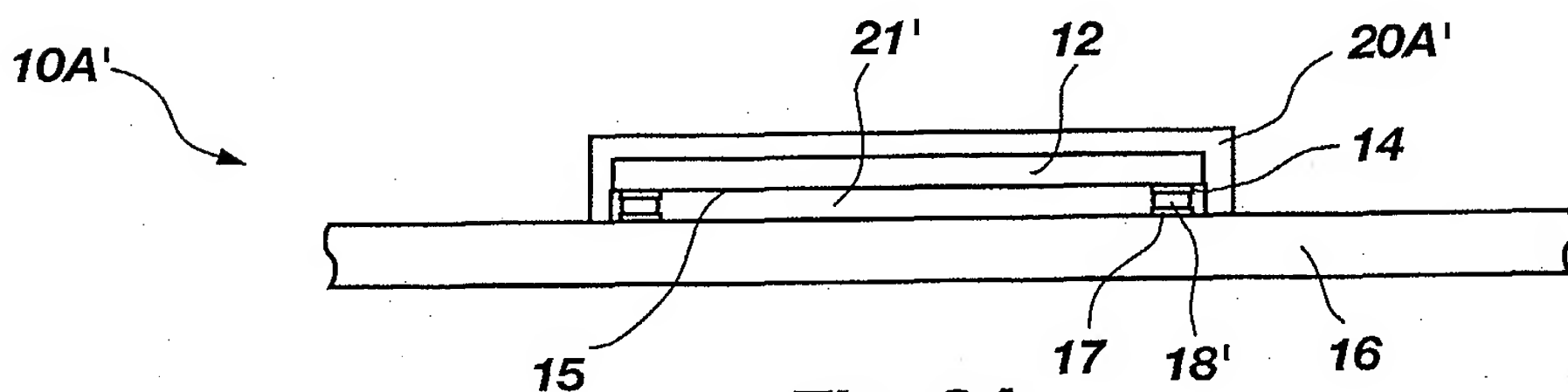


Fig. 2A

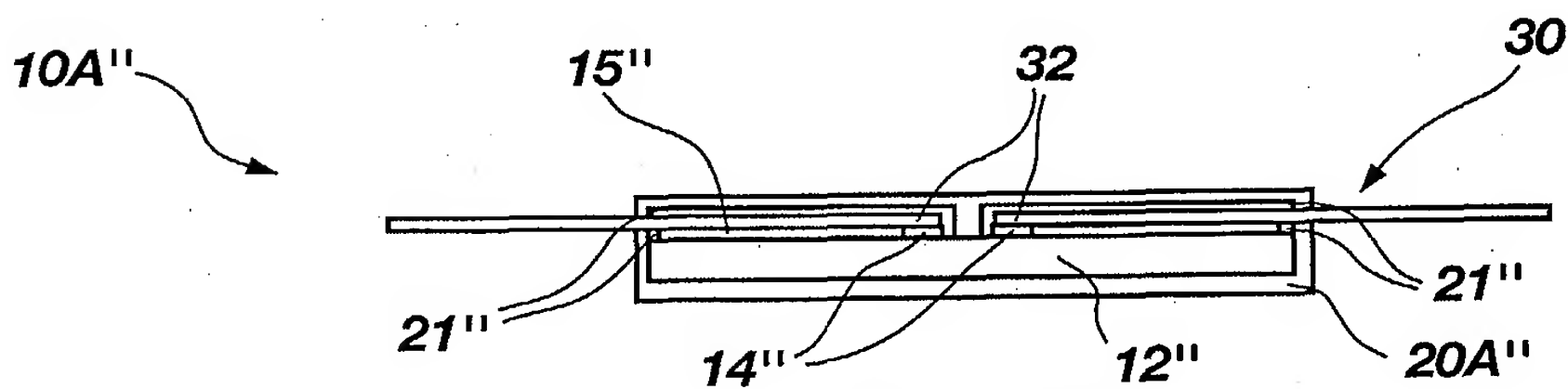


Fig. 3A

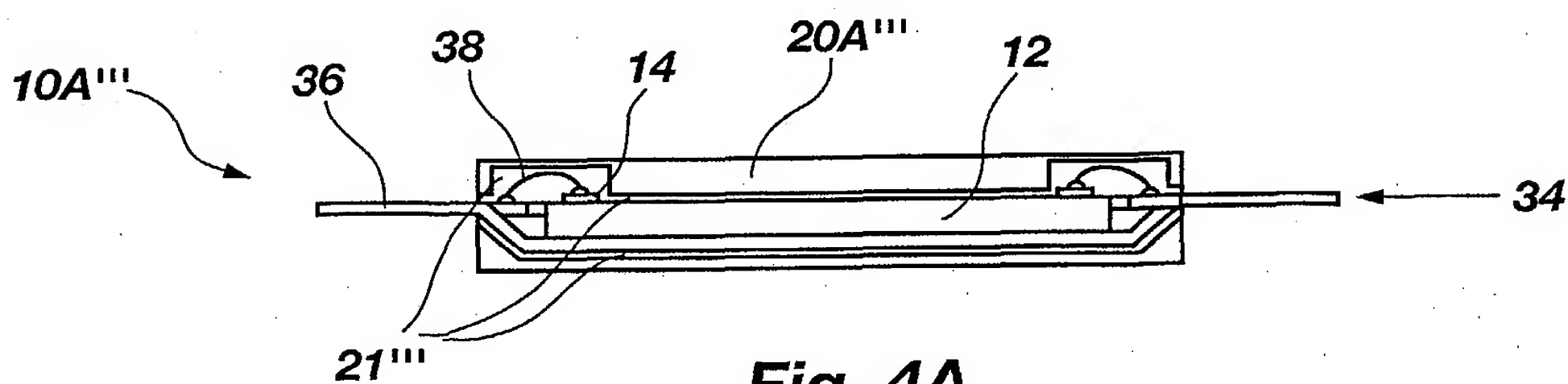


Fig. 4A

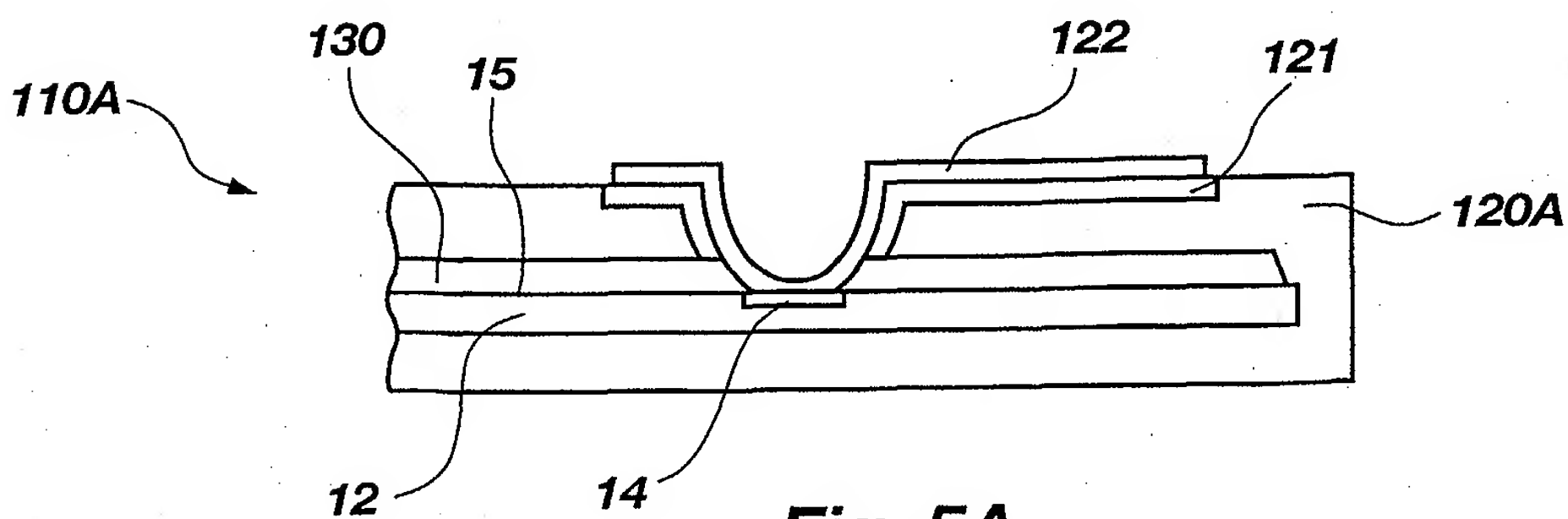


Fig. 5A

3/9

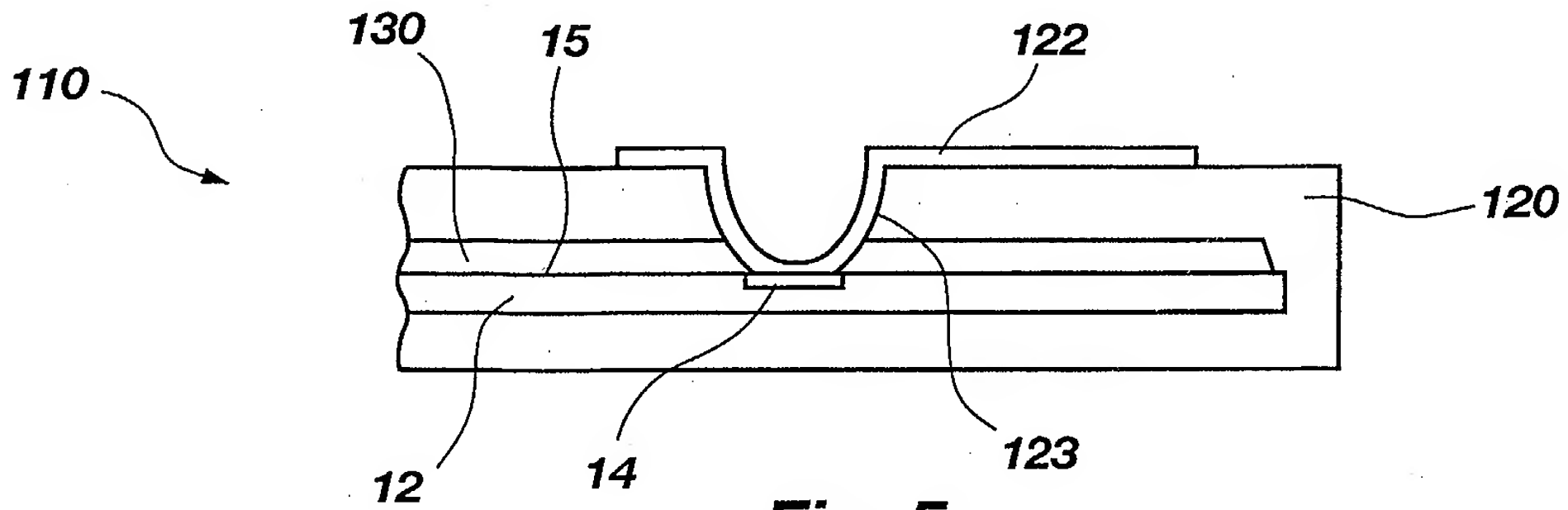


Fig. 5

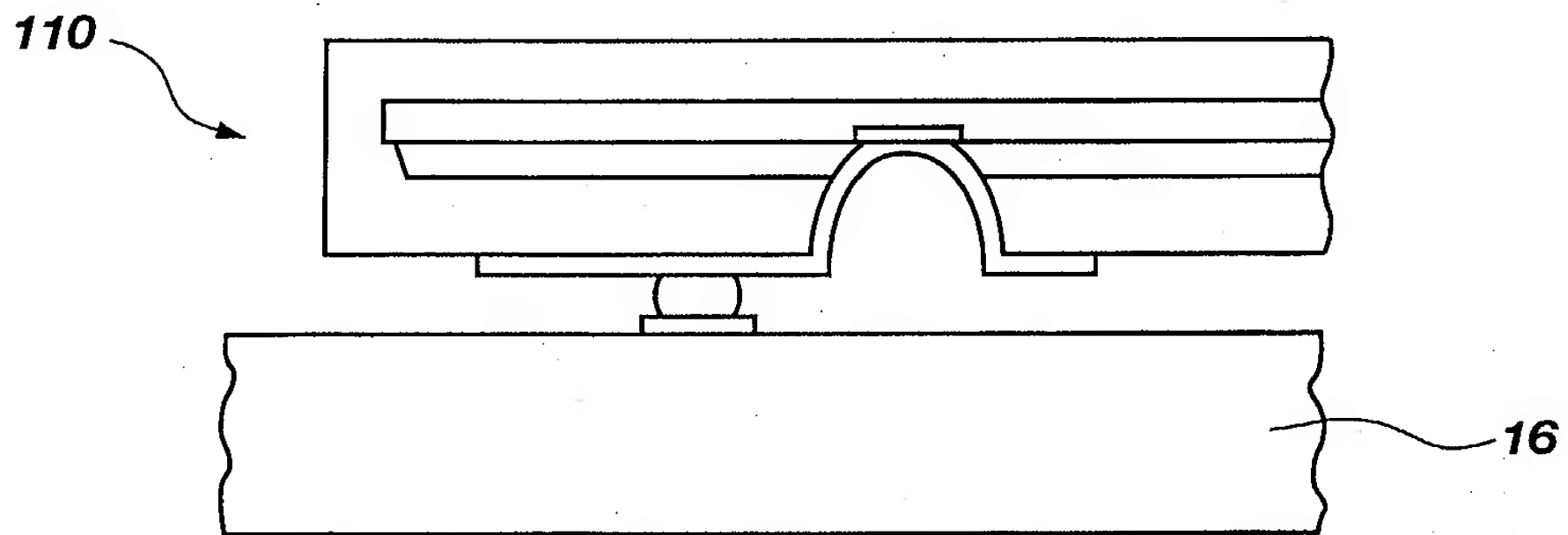


Fig. 6

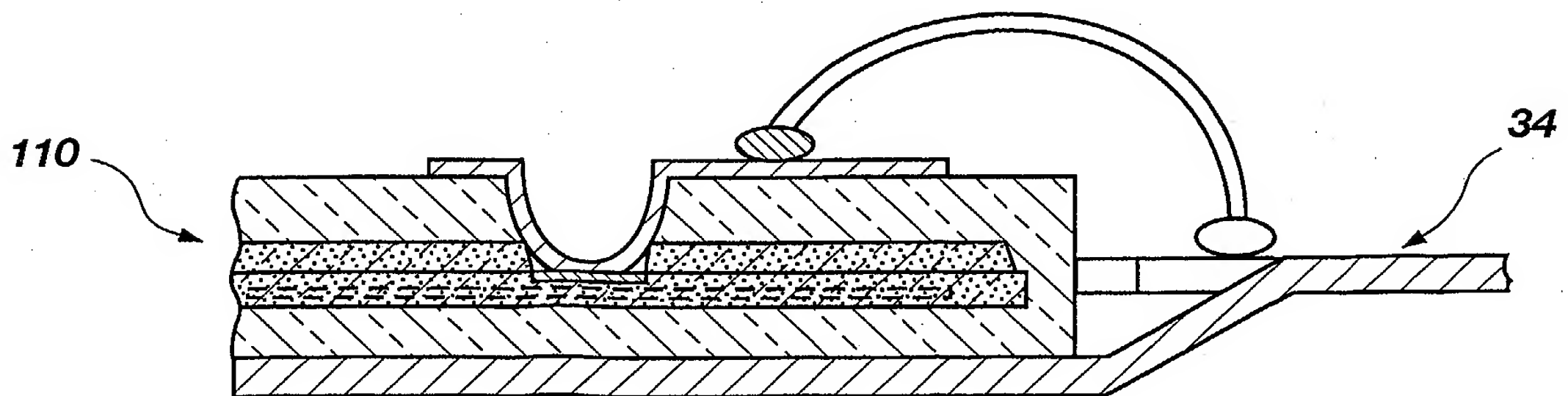


Fig. 7

TITLE: STEREOLITHOGRAPHIC METHODS FOR FABRICATING
HERMETIC SEMICONDUCTOR DEVICE PACKAGES AND
SEMICONDUCTOR DEVICES INCLUDING
STEREOLITHOGRAPHICALLY FABRICATED HERMETIC PACKAGES

Inventor: Warren M. Farnworth
Serial No.: Not Yet Assigned
Docket No.: 4212.1US

4/9

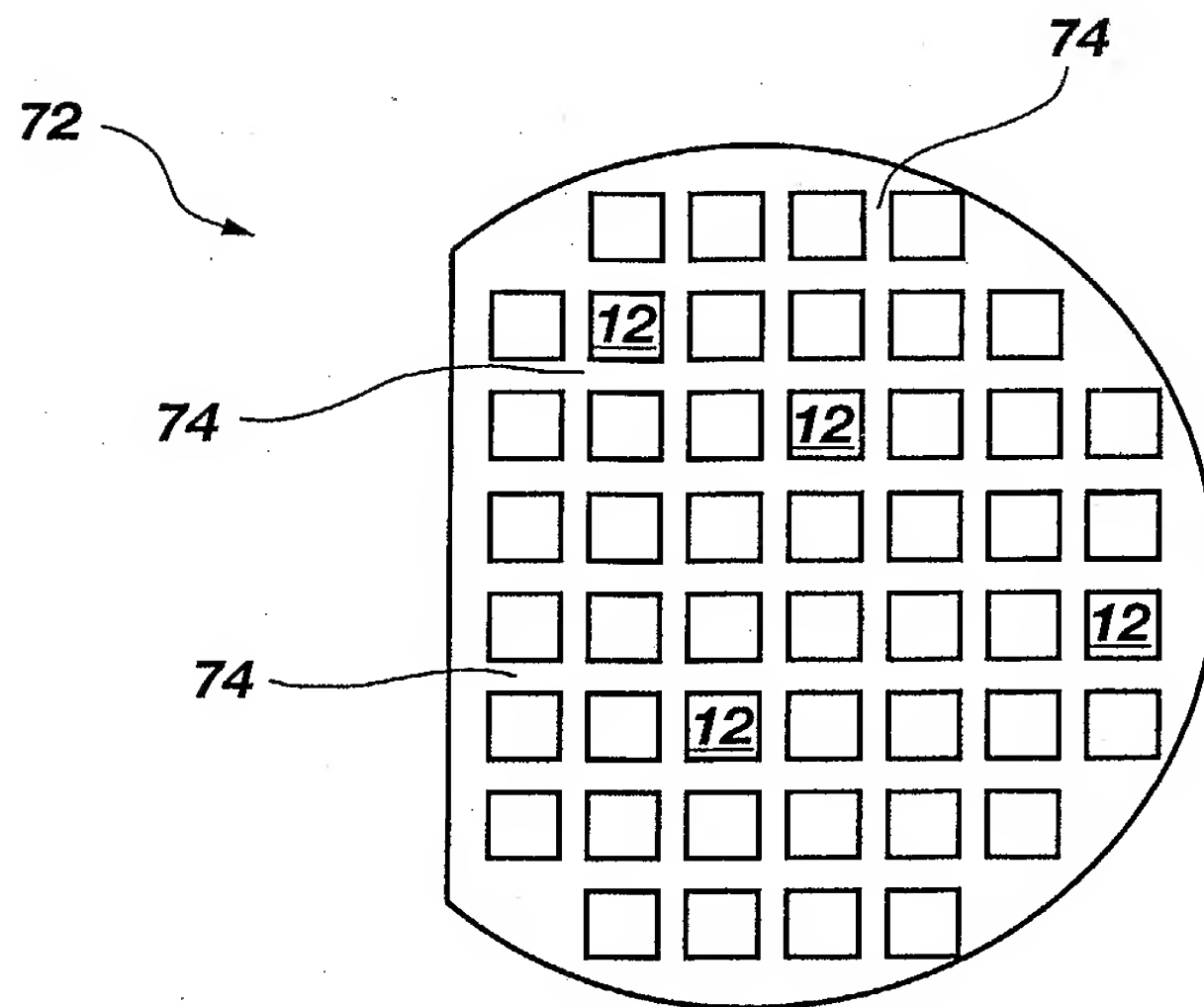


Fig. 8

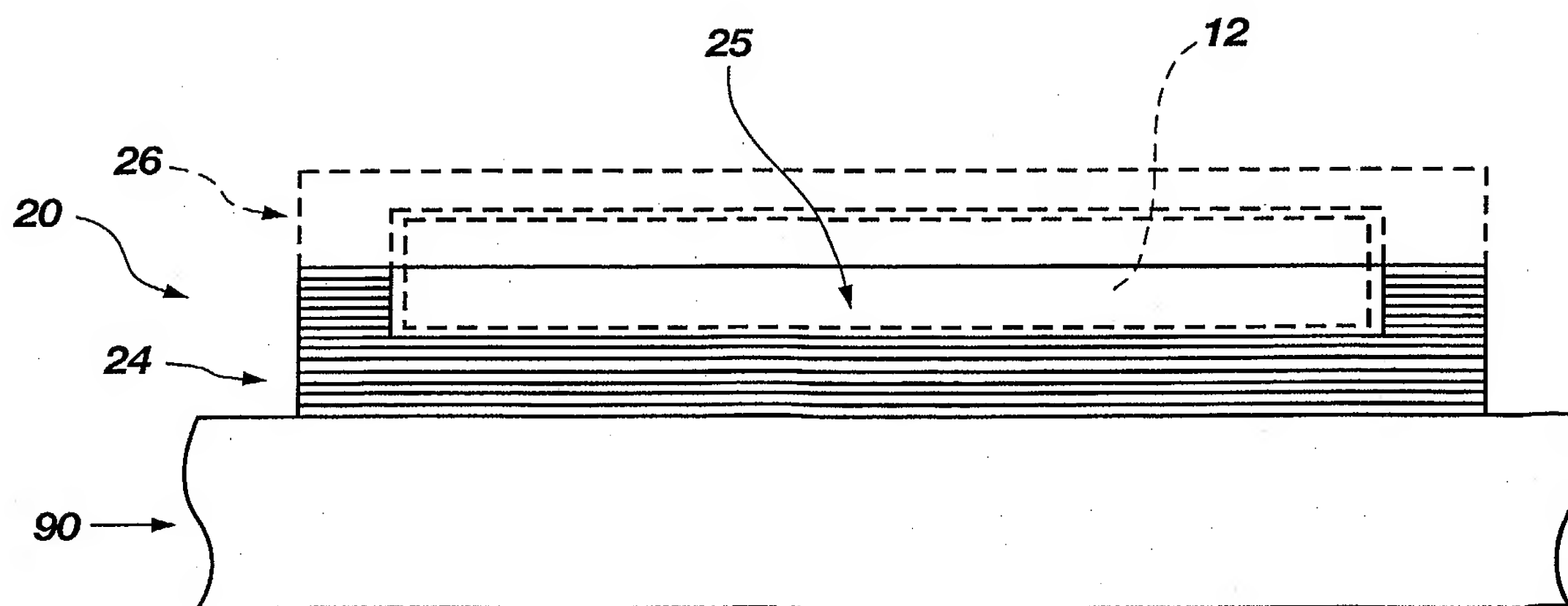


Fig. 18

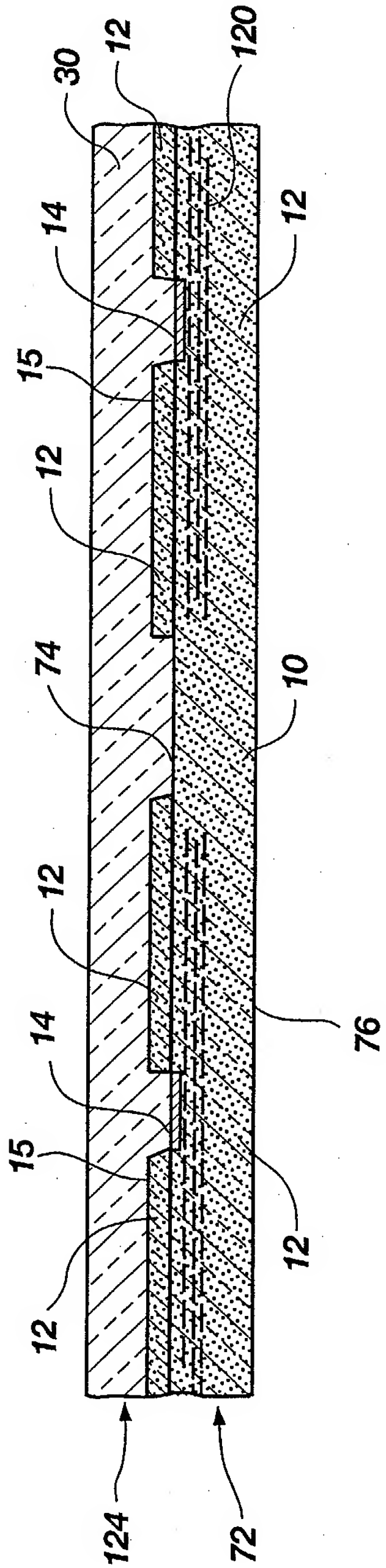


Fig. 9

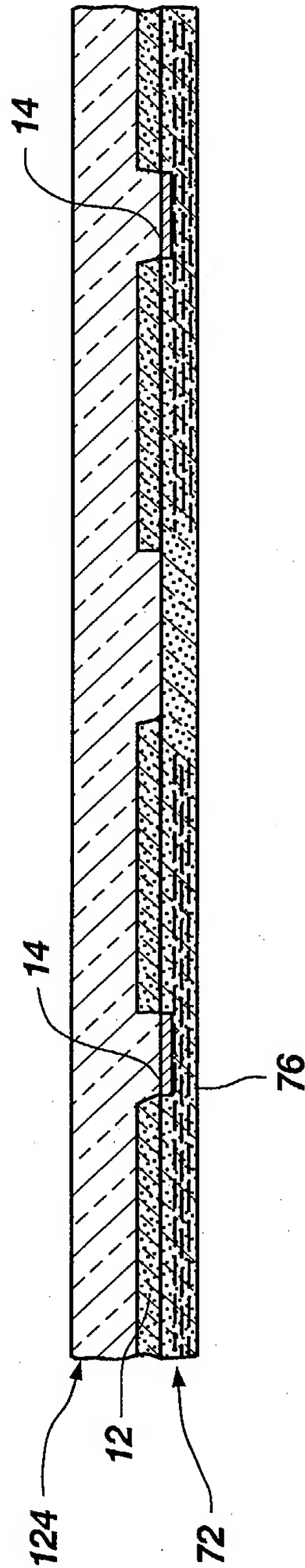


Fig. 10

6/9

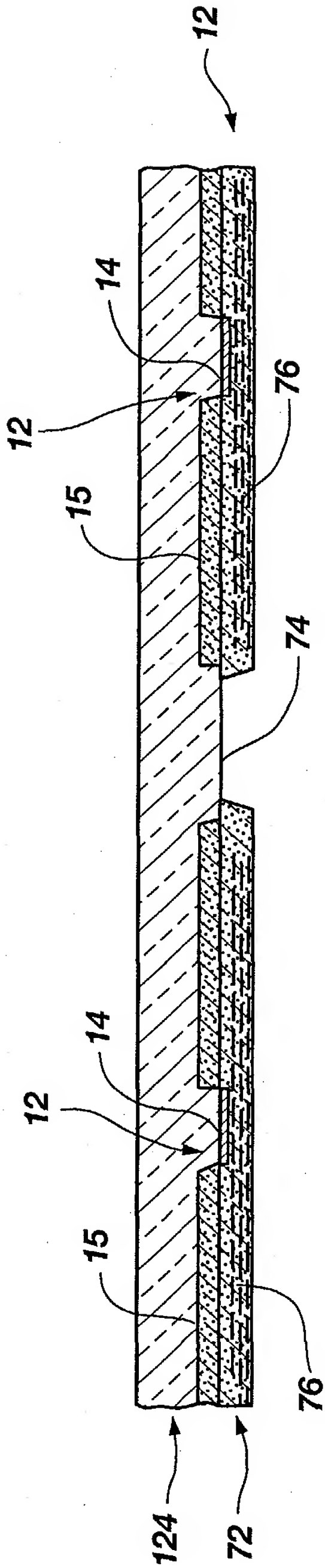


Fig. 11

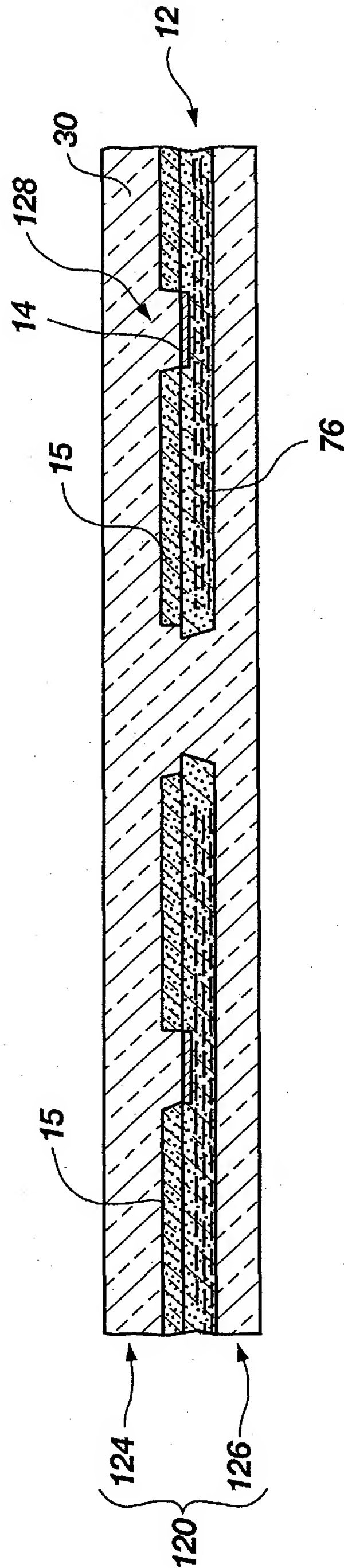


Fig. 12

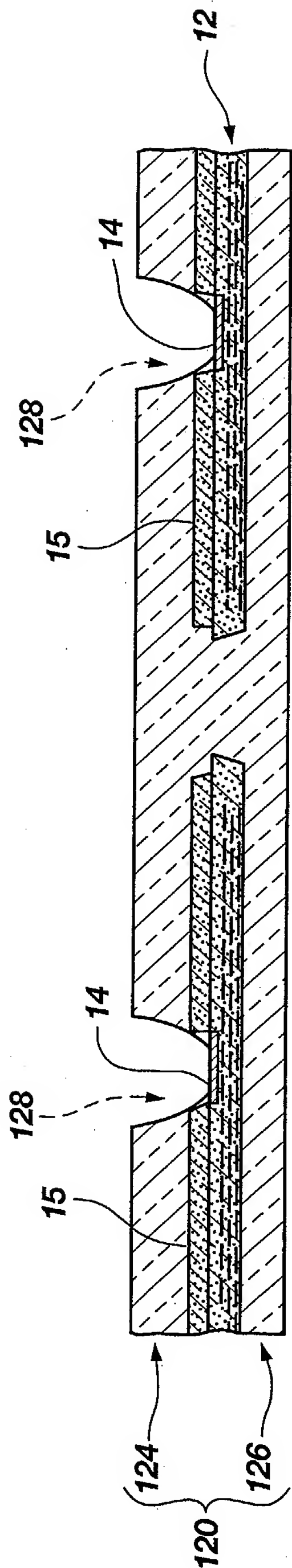


Fig. 13

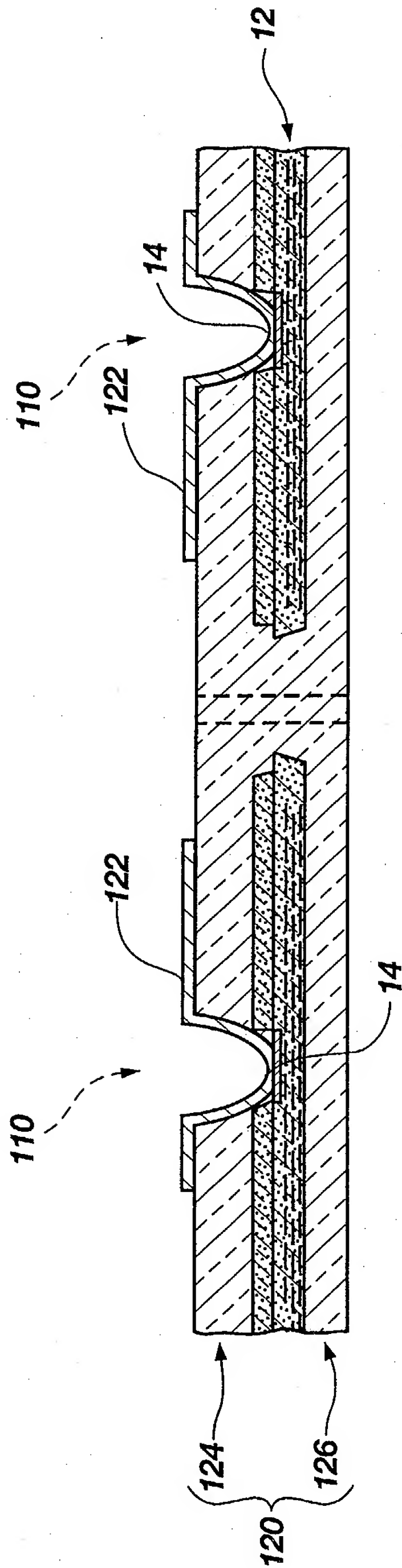


Fig. 14

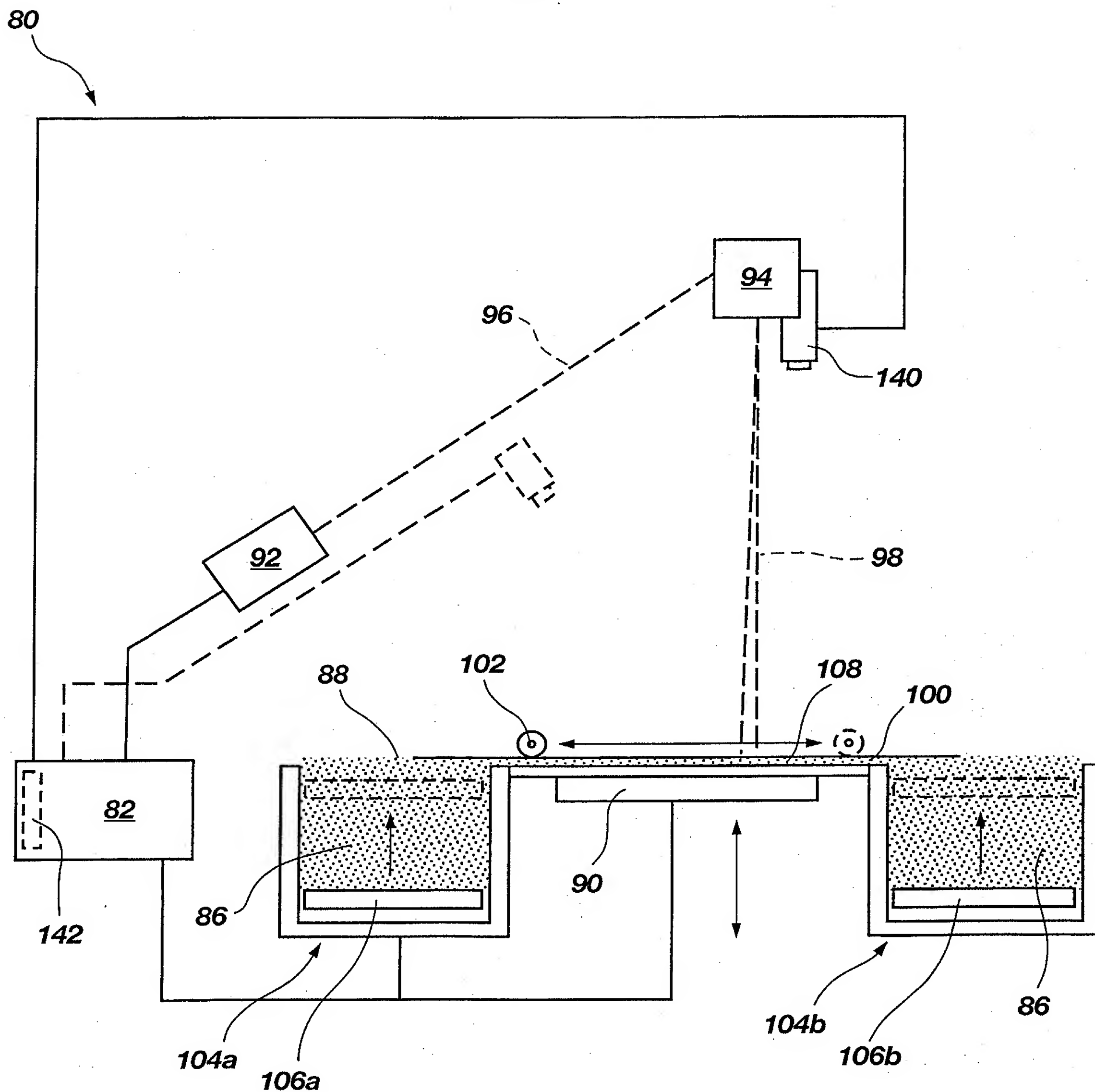


Fig. 15

9/9

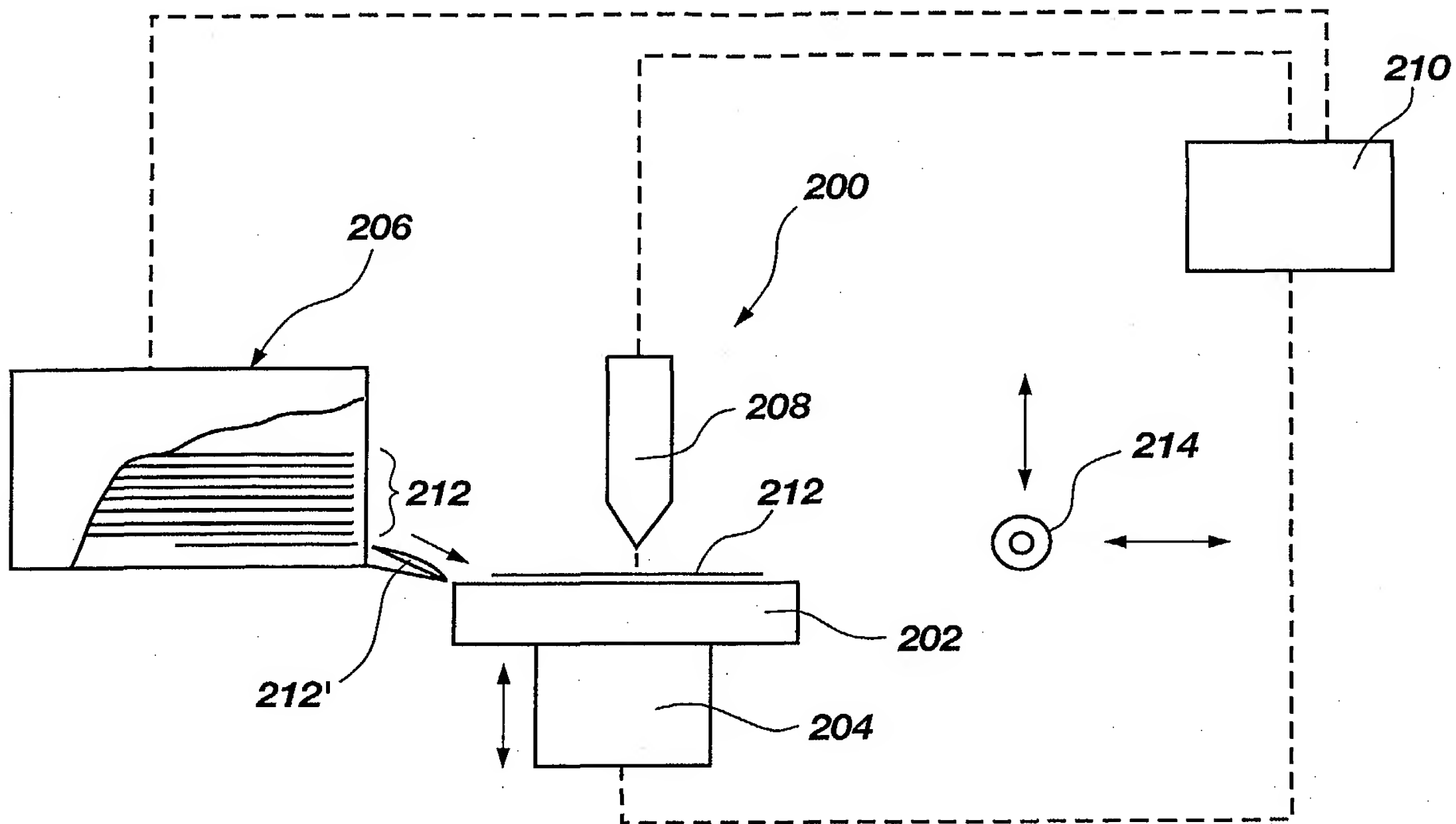


Fig. 16

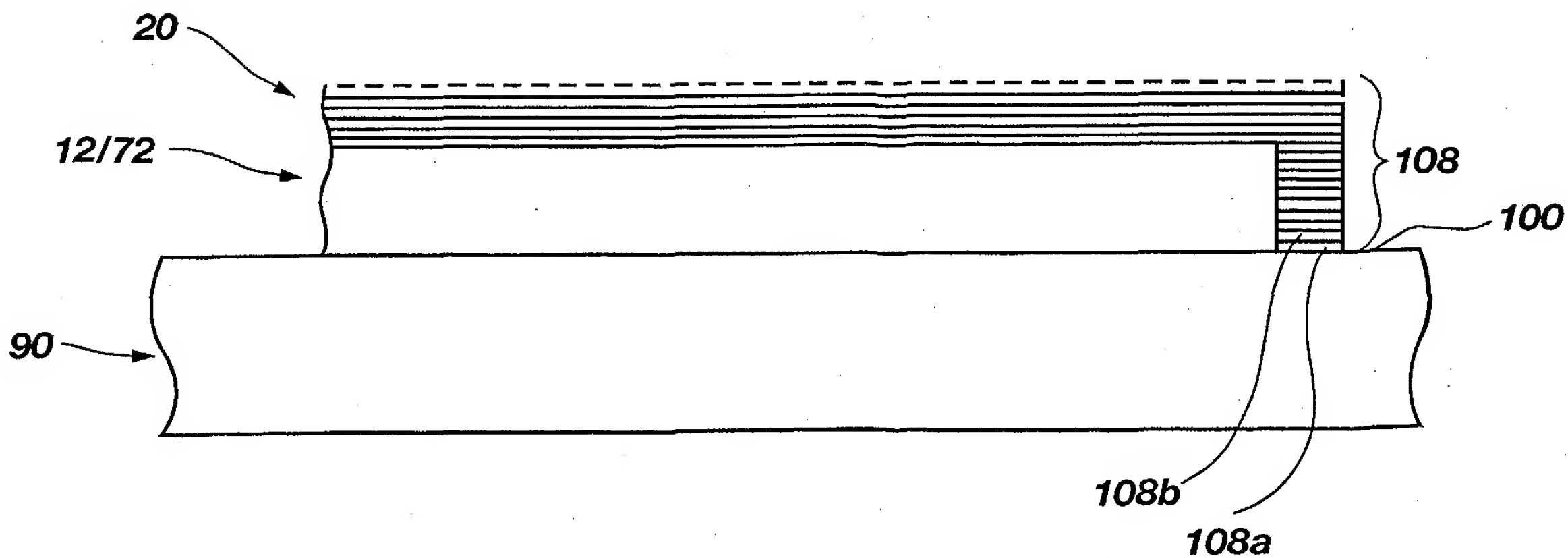


Fig. 17